

Abstracts

A 5.7 GHz Hiperlan SiGe BiCMOS voltage-controlled oscillator and phase-locked-loop frequency synthesizer

B.-U.H. Klepser, M. Scholz and J.J. Kucera. "A 5.7 GHz Hiperlan SiGe BiCMOS voltage-controlled oscillator and phase-locked-loop frequency synthesizer." 2001 Radio Frequency Integrated Circuits (RFIC) Symposium 01. (2001 [RFIC]): 61-64.

A SiGe BiCMOS 5.5-6.4 GHz frequency synthesizer is presented. The synthesizer consists of an oscillator with a phase noise of -110 dBc/Hz at 1 MHz offset, and a 10 GHz phase-lock-loop circuit with an in-band phase noise of -79 dBc/Hz. The power consumption of the ICs was 9 mW and 17 mW, respectively.

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